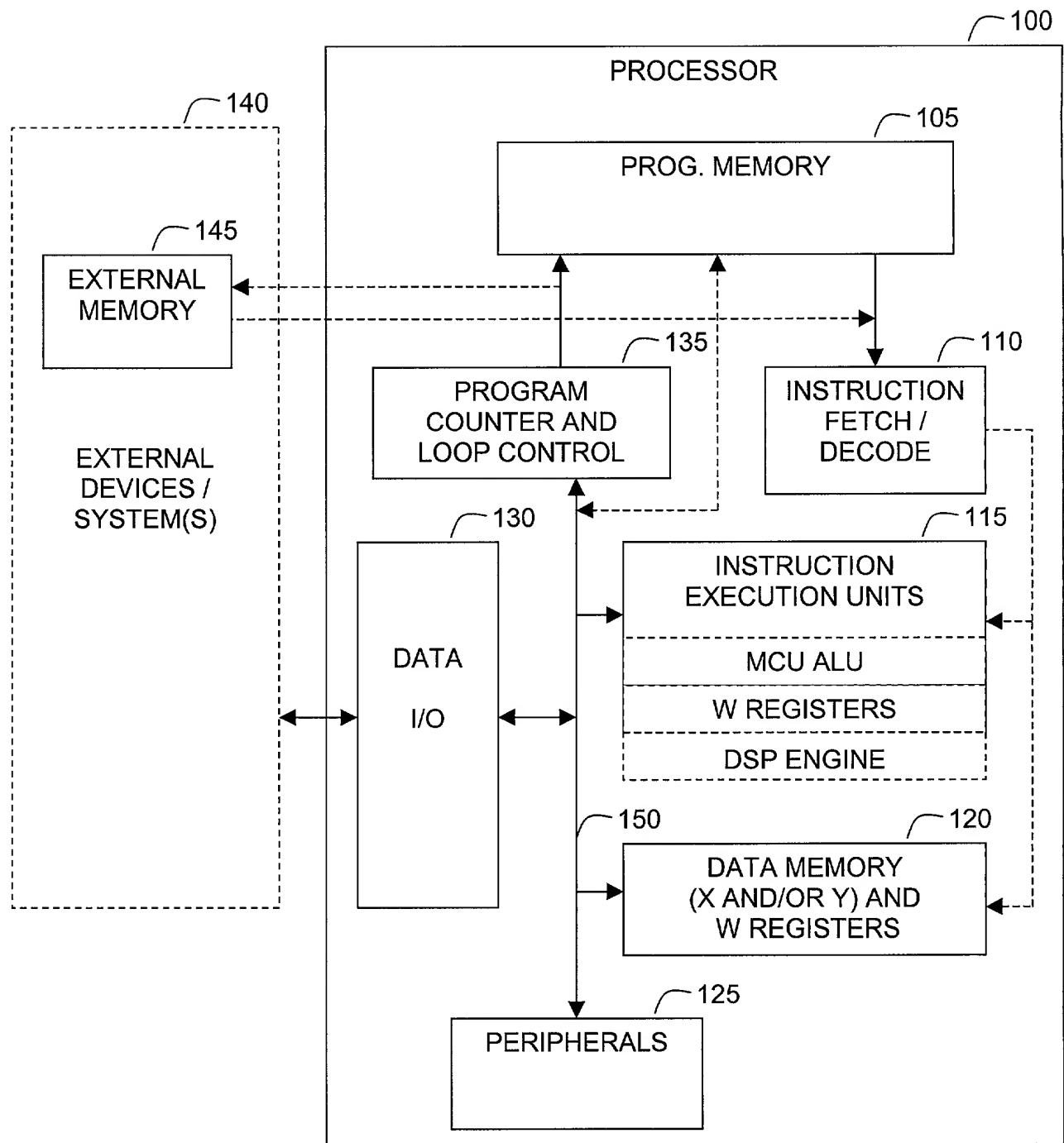
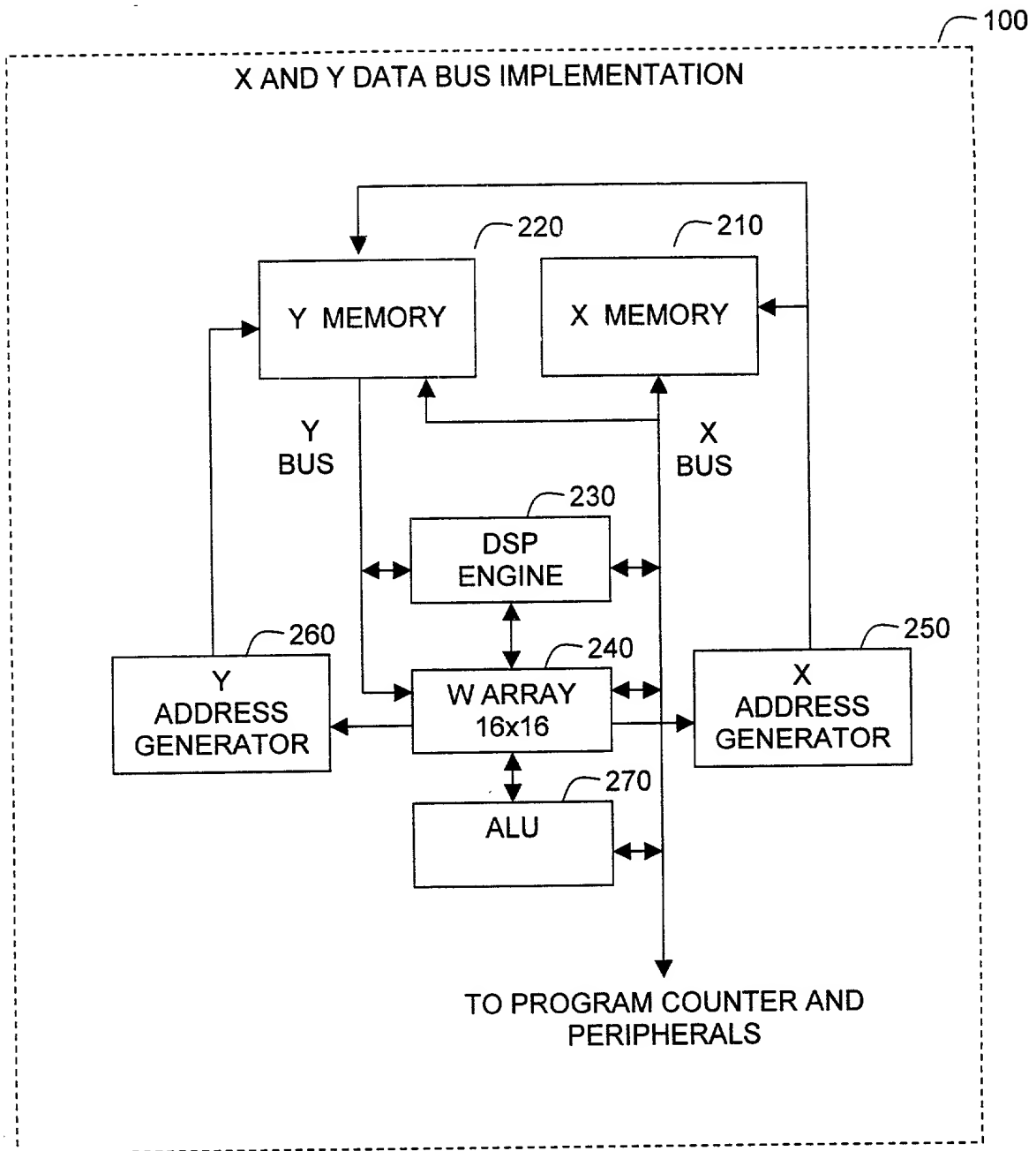


FIG. 1

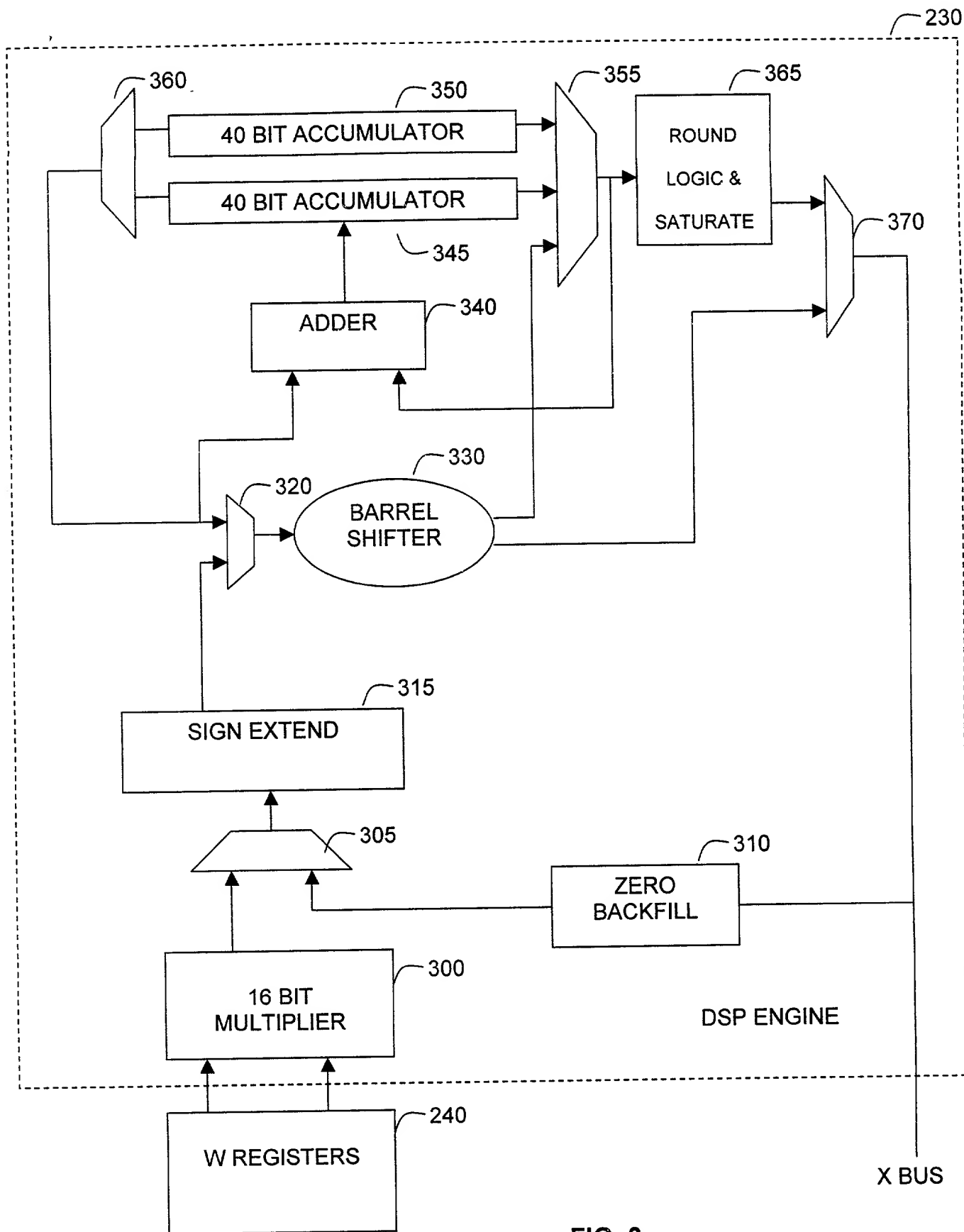


**FIG. 1**

FIG. 2



**FIG. 2**



**FIG. 3**

FIG. 4A

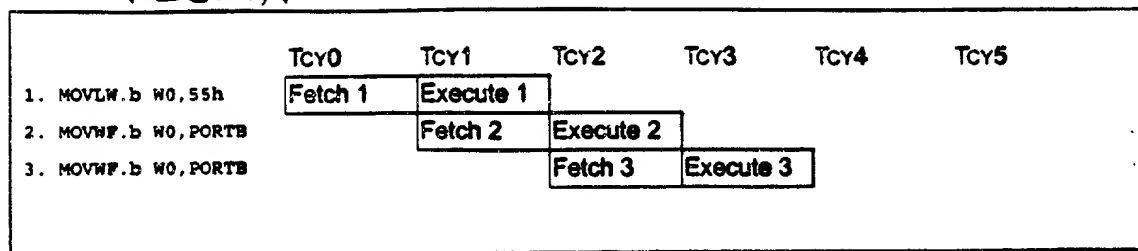
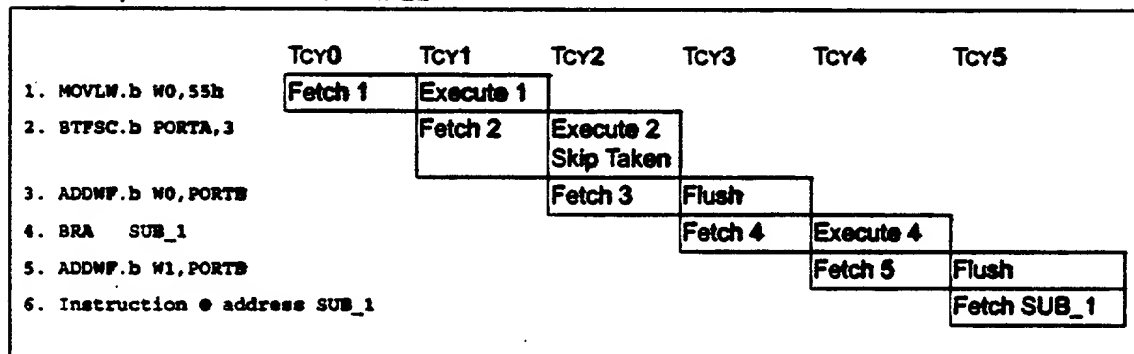
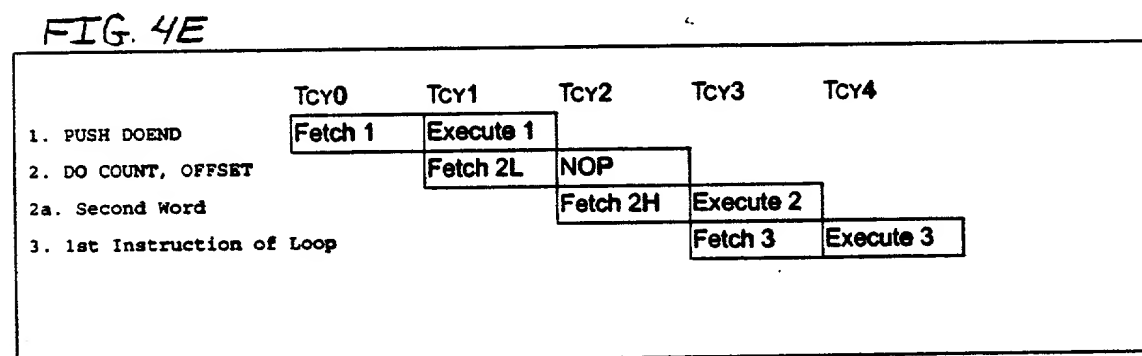
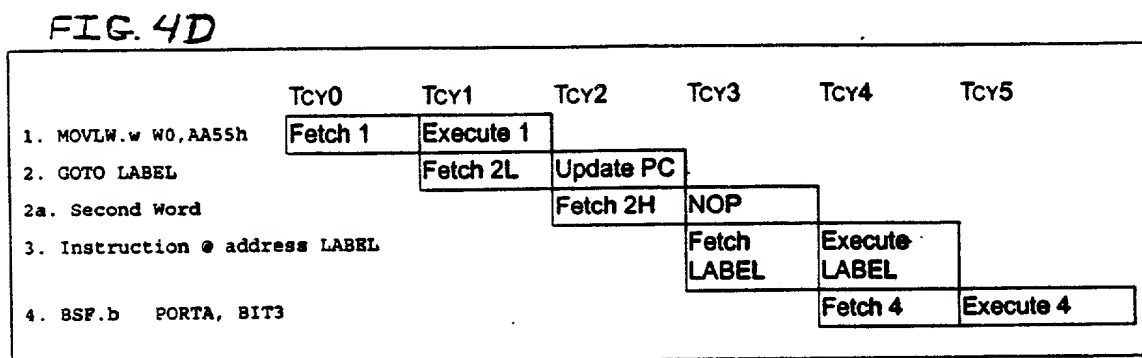
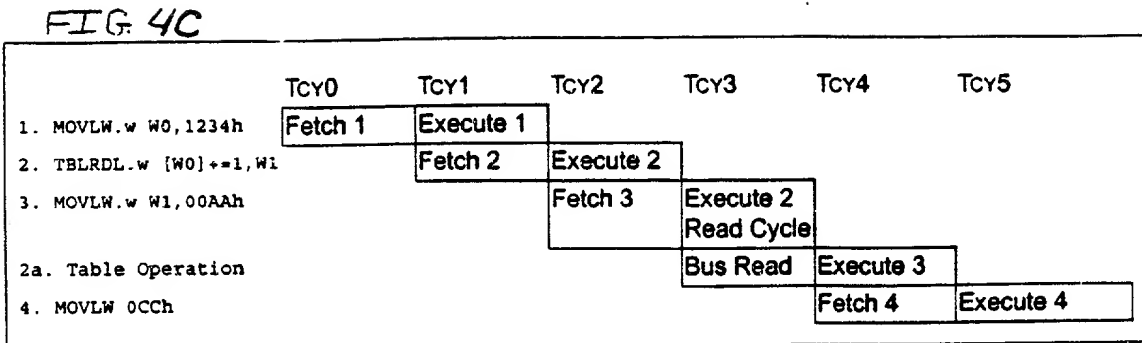


FIG. 4B





# dsPIC Core DOS

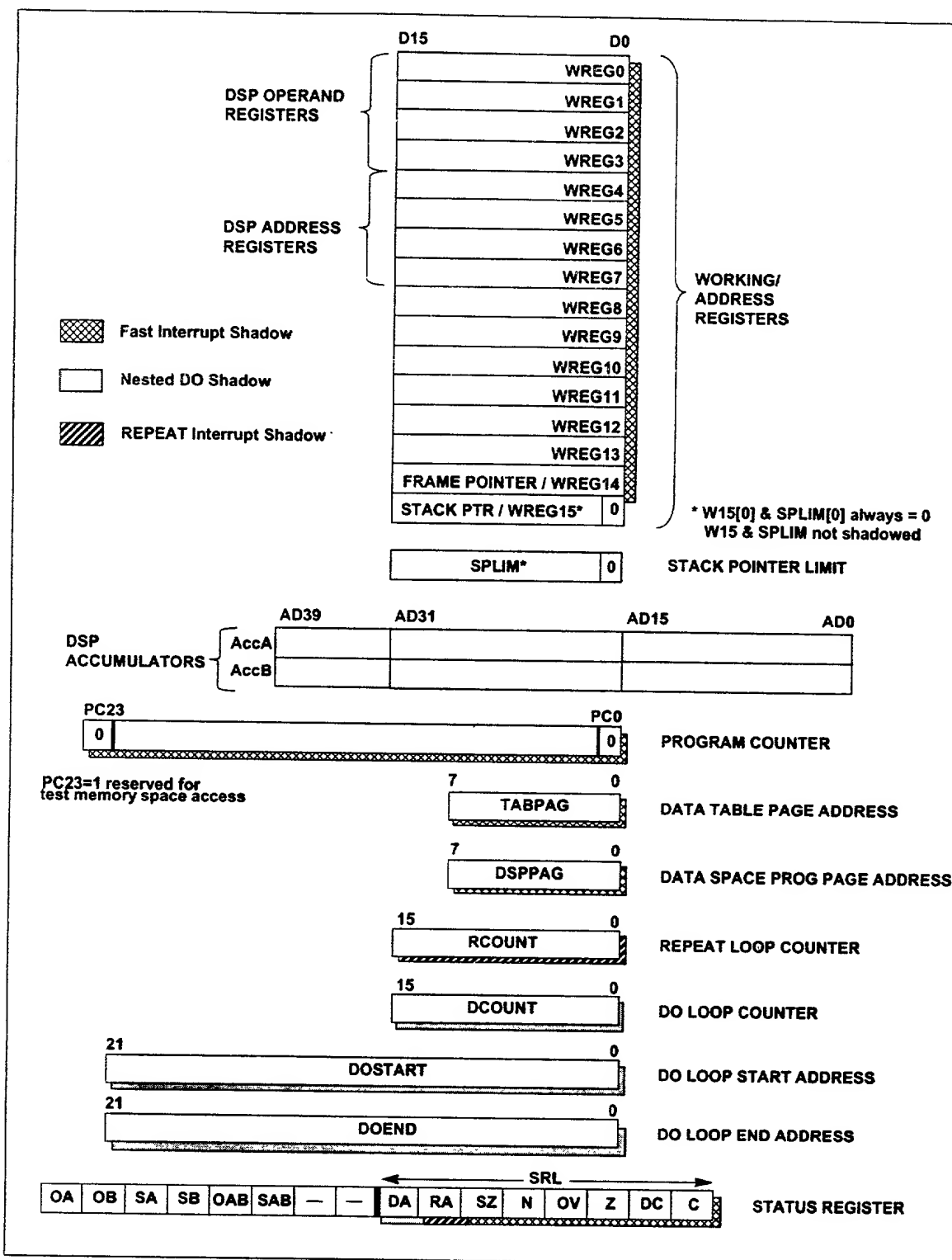


FIGURE 1-33: PROGRAMMERS MODEL

THIS DOCUMENT IS UNCONTROLLED UNLESS OTHERWISE STAMPED. It is the users responsibility to ensure this is the latest revision prior to using or referencing this document.	Page	SPEC. NO.	REV.
2000/12/08 Microchip Technology, Inc. CONFIDENTIAL AND PROPRIETARY	36 of 167	DOS-00200	A Draft

FIG 5